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(310) 207-3800 (310) 820-5988 (310) 820-5270

> BSTZ_MAIL@BSTZ.COM WWW.BSTZ.COM

INTELLECTUAL PROPERTY LAW

12400 WILSHIRE BOULEVARD SEVENTH FLOOR Los Angeles, California 90025-1030 OTHER OFFICES

ORANGE COUNTY/COSTA MESA, CA SILICON VALLEY/SUNNYVALE, CA DENVER, CO PORTLAND/BEAVERTON, OR SEATTLE, WA

January 10, 2008

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Re:

CERTIFICATE OF CORRECTION

U. S. Letters Patent No. 7,230,276

Issued: Jun. 12, 2007

For: Semiconductor Optical Device Having Current-Confined Structure

Inventor: Song et al.

Our File No. 002013.P115

Dear Sir:

Enclosed is a Certificate of Correction (two copies) for the above-referenced patent.

This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Eric S. I

Reg. No. 30,139

ESH/ms enclosures PTO Form- 1449

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO: 7,230,276

DATED: Jun. 12, 2007

INVENTOR(S): Song et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims, please delete claim 15 and insert claim 20 as follows:

"The device of claim 16, wherein the semiconductor layer constituting the gain region is one of a p-type semiconductor layer, an n-type semiconductor layer, and an undoped semiconductor layer."

In Claim #17, line 1, please delete "17 " and insert - - 16 - -. In Claim #18, line 1, please delete "17 " and insert - - 16 - -. In Claim #19, line 1, please delete "17 " and insert - - 16 - -.

MAILING ADDRESS OF SENDER:

PATENT NO. 7,230,276

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP 12400 Wilshire Blvd. 7th floor Los Angeles, CA 90025-1030

Certificate of Correction (PTO Form 1050)-Amended

1 of 1

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PATENT NO: 7,230,276

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INVENTOR(S): Song et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims, please delete claim 15 and insert claim 20 as follows: "The device of claim 16, wherein the semiconductor layer constituting the gain region is one of a p-type semiconductor layer, an n-type semiconductor layer, and an undoped semiconductor layer."

In Claim #17, line 1, please delete "17" and insert - - 16 - -.
In Claim #18, line 1, please delete "17" and insert - - 16 - -.
In Claim #19, line 1, please delete "17" and insert - - 16 - -.

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<u>1 of 1</u>